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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *my (no)*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *my (no)*

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY INDIA	SHEETS DRAWING 3	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 4
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Verified and Acknowledged  
 Examiner's Signature: *[Signature]* Initials: *[Initials]*

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TITLE  
 Low-jitter clock distribution circuit

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